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## **FI: Frontside Illuminated**

A CCD chip (on <u>ACIS</u>) in which X-rays are incident on the gate structure on the front of the chip. This blocks some low energy X-rays, but has lower background and (in the absence of parallel <u>CTI</u>) provides better energy resolution than <u>BI</u> (backside illuminated) chips.

See the ACIS entry for more details on the CCD chips.

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